

10/701,082

IN THE SPECIFICATION:

Please amend the second full paragraph on page 2 as follows:

A novel conventional computer configured to operate “approximation” between plural numeric values (between a set of numeric values and a set of numeric values) is disclosed in “A CMOS Stochastic Associative Processor Using PWM Chaotic Signals” described in IEICE Transactions on Electronics, Vol. E84 – C, No. 12, December 2001, pp1723-1729.